

Article Grid-Impedance-Based Transient Current Control for Offshore Wind Turbines under Low-Voltage Fault

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Abstract: In order to enhance the transient stability of offshore wind turbines (OWTs) in marine energy systems, the grid codes stipulate that OWTs should possess the low-voltage ride-through (LVRT) ability of being grid-tied and injecting reactive current during grid fault. However, the grid-side converter (GSC) of OWTs may lose stability under weak grid or severe fault conditions due to inaccurate current references. To address this issue, a novel transient current control method is proposed to improve the transient stability of permanent-magnet-synchronous-generator (PMSG)-based OWTs. The feature of DC-link overvoltage is investigated and is alleviated by utilizing the GSC's overcurrent capacity and chopper. Additionally, the equivalent circuit of the PMSG-based OWT connected to the onshore grid is derived based on Thevenin's theorem. The feasible current region (FCR) is then determined, taking into account the GSC capacity, pre-fault power ability, LVRT requirement, and synchronization stability. Furthermore, a grid-impedance-based transient current control method is designed to enhance the fault ride-through performance and mitigate power oscillation of the OWT under various transient grid impedance and fault conditions. Finally, a simulation model is conducted using PSCAD v4.6.3 software to validate the effectiveness of the proposed method.

Keywords: offshore wind turbine; permanent magnet synchronous generator; low-voltage fault; transient current control; feasible current region; grid impedance

1. Introduction

To address the environmental pollution and fossil fuel crisis, there has been a significant development in renewable energy over the past decade, which is expected to play a crucial role in the future energy system [1,2]. Among renewable energy sources, wind power accounts for 23% of the total global renewable energy, ranking second after hydrogen power [3]. Offshore wind power, as a form of marine renewable energy, has gained considerable attention due to its abundance of wind resources, proximity to load centers, and minimal land occupation [4,5]. Extensive research has been conducted on the transmission schemes for offshore wind turbines, including high-voltage alternative current (HVAC) and high-voltage direct current (HVDC) [6]. HVDC is more economically and operationally efficient when the transmission distance exceeds 50 km. However, HVAC is widely used for offshore power transmission as most offshore wind farms are located within 50 km of the onshore grid. In terms of generator types, offshore wind turbines are typically equipped with either doubly fed induction generators (DFIGs) or permanent magnet synchronous generators (PMSGs), both of which are connected to the grid through power electronic devices [7]. PMSGs are gaining popularity in the marine sector due to their advantages of not requiring a gearbox, higher power scalability, and greater flexibility in supporting the grid [8]. In this study, we focus on PMSG-based offshore wind turbines as the research objective.

The PMSG-based offshore wind turbine (OWT) is connected to the power grid through a power electronic device (PED) called the fully rated converter [9]. As the penetration



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Copyright: © 2024 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). rate of offshore wind power increases, the power grid structure undergoes significant changes, with a shift towards the use of massive PEDs instead of synchronous generators (SGs). However, PEDs have poorer overcurrent and overvoltage capabilities compared to traditional SGs, leading to notable frequency and voltage instability. Additionally, the overhead line in the onshore grid is prone to grounding faults, which can result in DC-link overvoltage and off-grid accidents for offshore wind turbines, as the onshore grid voltage decreases [9]. This further exacerbates the issues of active power reduction and onshore grid voltage fluctuation. To mitigate these challenges, many countries have implemented grid codes that mandate wind turbines to possess low-voltage ride-through (LVRT) abilities. These abilities enable OWTs to keep grid-tied and provide reactive current to the onshore grid during faults [10–12].

There are three main LVRT methods for offshore wind turbines (OWTs): software modified method, hardware modified method, and a combination of software and hardware approaches [13]. The software modified method can be further classified into modified converter control [14,15], improved pitch angle control [16], and algorithm-based control [17,18]. On the other hand, the hardware modified method involves the addition of external devices, such as a chopper circuit [19,20], flexible AC transmission system (FACTs) devices [21,22], and energy storage systems [23]. Furthermore, researchers have also explored the combination of software and hardware approaches [24,25]. The gridside converter (GSC) plays a crucial role in determining the grid-tied characteristics of PMSG-based OWTs. The output power can be controlled by adjusting the current reference of the GSC, as the active and reactive currents are directly proportional to the active and reactive power, respectively. Under low-voltage fault conditions, the GSC switches to the reactive current injection priority mode instead of the active current injection priority mode used in steady-state operation [26]. However, the active current and reactive current are limited by the capacity of the GSC. Consequently, in severe fault scenarios, the OWT may experience a significant reduction in active current or active power, leading to electricity waste and frequency fluctuations. To mitigate the reduction in active current during grid faults, researchers have depicted the feasible power region of the converter for better power allocation [27,28]. Additionally, proper current references are provided to the GSC's controller based on the feasible current region, taking into account the converter capacity and grid code requirements [29].

In the previously mentioned studies, the LVRT strategy is primarily developed for the strong power grids, where the power system has a robust ability to withstand voltage dips. However, due to factors such as long-distance transmission lines and high levels of renewable energy integration, the onshore grid may exhibit weaker characteristics, including low inertia, high impedance, unstable voltage, and limited resistance to interference. As a result, greater demands are being placed on the LVRT technology of OWTs. When operating in a weak grid with high impedance or during severe low-voltage faults, the OWT is prone to losing synchronization with the onshore grid due to improper current references to converters, resulting in transient current and power oscillations [30]. In order to address this issue, a current reference control scheme that considers grid impedance and a feasible current region during faults has been proposed based on the analysis of stable equilibrium points [31]. Additionally, pre-fault power ability is also an important factor affecting the feasible current region. Therefore, it is valuable to design an accurate feasible current region to better allocate the current references of GSC during low-voltage faults.

To address the aforementioned issue, this paper proposes a grid-impedance-based transient current control method to mitigate DC-link overvoltage and support system stability with different grid strengths. The key contributions of this paper are summarized as follows:

 This paper explores the DC-link overvoltage trajectory with different grid voltage dip conditions and proposes a solution by utilizing GSC's short-time overcurrent capacity and chopper.

- (2) To provide accurate guidance for transient current control, the feasible current region is proposed through visualization of GSC capacity, pre-fault power ability, LVRT requirement and synchronization stability.
- (3) A grid-impedance-based transient current control is utilized to enhance the LVRT performance of OWTs and mitigate power oscillations under different voltage dip and grid impedance conditions.

The remainder of this paper is organized as follows. Section 2 presents the detailed model of PMSG-based OWTs and analyzes the feature of DC-link overvoltage. Section 3 establishes the feasible current region considering the four aforementioned constraints. Section 4 describes the grid-impedance-based transient current control strategy for GSC during fault periods. Section 5 presents case studies to validate the proposed method. Finally, the conclusions are presented in Section 6.

2. Problem Description and Analysis

2.1. System Description

As depicted in Figure 1, power grids integrated with offshore wind farms are composed of offshore wind turbines, submarine cables, transformers, transmission lines, and onshore grids. Offshore wind farms are usually composed of 30–50 OWTs, and the OWTs are collected by several submarine cables to offshore stations. The transformer in offshore station can improve the voltage from 35 kV to 220 kV, and then the electricity is transmitted from the offshore station to the onshore grid through the transmission line. For convenience, a single-machine equivalent model is used to study the transient characteristics of offshore wind farms. A PMSG-based OWT is also depicted in Figure 1, which contains a turbine, generator, chopper, DC-link capacitor, machine-side converter (MSC), and GSC. MSC can convert alternative current to direct current, and GSC is used to convert direct current to alternative current. The chopper circuit is composed of a power electronic switch and resistor R_{ch} , which can absorb surplus power under low-voltage fault. Pitch angle control, MSC control, GSC control, and chopper control jointly form the control system of the OWT.



Figure 1. Typical structure of OWTs connected to onshore grid.

In the event of a symmetrical low-voltage fault in the onshore grid, both the voltage of the onshore grid and the OWT are influenced. The active current may decrease, and the DC-link voltage (U_{dc}) exceeds the withstand value of converter, which is harmful to the device and further increases the risk of off-grid accidents in the OWT.

2.2. LVRT Requirement and Control Method

If the onshore grid suffers from low-voltage fault, the point of integration (POI) voltage drops with the reduction of onshore grid voltage vector (U_s). According to the fault position in the simplified topology of Figure 2, the low-voltage fault can be divided into three types, namely nearby fault, middle-distance fault, and distant fault. In Figure 2, V_w is the wind velocity, U_w is the POI voltage vector, and Z_s is the grid impedance vector of onshore grid. R_s and X_s are the resistance and reactance of onshore grid, respectively, and $Z_s = Z_s \angle \phi = R_s + jX_s$. Z_s and ϕ are the amplitude and phase angle of Z_s .



Figure 2. Simplified topology of an OWT connected to an onshore grid.

When the low-voltage fault occurs in the power grid, the wind turbine may be disconnected from the grid, causing negative impacts to the voltage and frequency stability. To mitigate the negative influences, grid codes in most countries require the wind turbine to have the LVRT ability that the wind turbine is grid-tied and provide reactive current to the power grid for voltage support. According to China grid code [32], the required LVRT curve for wind turbine is shown in Figure 3. When faults or disturbances cause the voltage sag at POI connected to the OWT, the wind turbine should be gird-tied and provide reactive current if the grid voltage is between 0.2 and 0.9 p.u. And wind turbine is allowed to be off-grid while the grid voltage drops under 0.2 p.u. shown in the pink zone of Figure 3. The blue line is the reactive current curve according to grid code requirement.



Figure 3. LVRT requirement of grid code.

The LVRT methods of onshore wind turbines are also used in OWTs [33]. The voltage dip amplitude of POI depends on the fault type. According to the grid code, when three-

phase fault occurs, the OWT should have the ability of dynamic reactive power support, which requires the OWT to produce dynamic reactive current as follows:

$$I_{\text{qref}} = -k_{q} \cdot (0.9 - U_{w}) \cdot I_{qN} + I_{q0} \tag{1}$$

where I_{qref} is the reference of reactive current I_q ; I_{q0} is the initial value of reactive current, of which the value is 0. I_{qN} is the rated value of reactive current; and K_q is the reactive current coefficient, ranging from 1.5 to 3. U_w is the POI voltage, namely the amplitude of U_w .

If the OWT is in LVRT state, the GSC controller adopts reactive priority mode, and the active current reference can be calculated as

$$I_{\rm dmax} = \sqrt{I_{\rm m}^2 - I_{\rm qref}^2} \tag{2}$$

where I_{dmax} is the maximum value of active current I_d and I_m is the maximum current of GSC.

As the initial part of OWTs, GSC determines the grid-tied characteristics of OWTs. The conventional LVRT control structure of GSC is shown in Figure 4. GSC adopts the voltage-oriented-vector-based control method, and the voltage vector is oriented towards the d-axis as follows:

$$\begin{pmatrix}
U_{wd} = U_w \\
U_{wq} = 0
\end{cases}$$
(3)

where U_{wd} is the d-axis component of U_w and U_{wq} is the q-axis componet of U_w .



Figure 4. Detailed LVRT control structure of GSC.

The angle δ is obtained via phase-locked loop (PLL) and is oriented with the d-axis voltage of the onshore grid. The active and reactive power of GSC are

$$\begin{cases} P = 1.5 U_{\rm wd} I_{\rm d} \\ Q = -1.5 U_{\rm wd} I_{\rm q} \end{cases}$$

$$\tag{4}$$

When the OWT is in steady state, the dq-axis voltage is calculated according to the dq-axis coordinate system:

$$\begin{cases} U_{wd} = R_s I_d - X_s I_q + U_s \cos \delta = U_w \\ U_{wq} = R_s I_q + X_s I_d - U_s \sin \delta = 0 \end{cases}$$
(5)

To avoid the influence of coupling voltage, the feed-forward decoupling control is introduced. Adopting PI controller in the current inner loop and ignoring R_s , the closed-loop control expressions are as follows:

$$U_{id} = G_i(s)(I_{dref} - I_d) - \omega_w L_f I_q + U_{wd}$$

$$U_{iq} = G_i(s) \left(I_{qref} - I_q \right) + \omega_w L_f I_d - U_{wq}$$
(6)

where $G_i(s)$ is the transfer function of PI controller; U_{id} and U_{iq} are the d-axis component and q-axis component of GSC voltage; L_f is the filter inductance; and ω_w is the angular frequency of the POI voltage.

In Figure 4, GSC adopts a double closed-loop control method, containing an out-loop module and a current inner-loop control module. The out-loop module aims to maintain the DC-link voltage or reactive power at a target value and sends current reference to the inner-loop control module. The DC-link voltage is controlled by active current, and reactive power is controlled by reactive current. Based on the state flag, the GSC controller can switch between active current priority mode and reactive current priority mode. When the OWT is in steady state (state 1), GSC operates in active current priority mode with reactive current maintained at zero. While in LVRT state (state 2), GSC adopts reactive current priority mode to produce active current to support onshore grid voltage.

2.3. DC-Link Overvoltage during Fault

As is stated above, two initial factors affecting the operation security of OWTs are DC-link voltage and GSC current during low-voltage state. When onshore grid voltage drops deeply, the DC-link overvoltage occurs because of the unbalanced power between MSC and GSC. Thereby, the chopper circuit is activated to absorb the unbalanced power for the suppression of DC-link overvoltage via resistance energy consumption.

Figure 5 reveals the transient power flow of the OWT during low-voltage fault. Wind power is transmitted from MSC to GSC. According to the power balance law, the DC-link power is calculated according to

$$P_{\rm DC} = P_{\rm MSC} - P_{\rm GSC} - P_{\rm Chop} \tag{7}$$

where P_{MSC} and P_{GSC} are the active power of MSC and GSC, respectively. P_{Chop} is the absorbed power of chopper. Chopper circuit is disconnected during steady state and put into operation during low-voltage fault.



Figure 5. Transient power flow on the DC side of the OWT.

The instantaneous power on the DC-link side of GSC is $U_{dc}I_{dc}$. Ignoring the transformer and line losses, $U_{dc}I_{dc} = 1.5U_wI_d$ is met in Figure 5. The DC-link voltage U_{dc} is directly proportional to the active current I_d , so U_{dc} can be controlled through I_d . According to the outer-loop control module of DC-link voltage, the active current reference in steady state is

$$I_{\rm dref} = K_{\rm up} \left(U_{\rm dc} - U_{\rm dcref} \right) + K_{\rm ui} \left/ \left(U_{\rm dc} - U_{\rm dcref} \right) dt$$
(8)

where K_{up} and K_{ui} are the proportional and integral coefficients of PI controller, respectively; U_{dcref} is the reference value of the DC-link voltage.

Considering that the response speed of the current inner-loop control is relatively fast, the active current I_d strictly follows its reference value. Therefore, ignoring the dynamic process of the current inner-loop control, the active current can be directly replaced by its reference value. Neglecting the power loss, the power balance equation of GSC can be expressed as

$$P - 1.5U_{\rm w}I_{\rm d} = U_{\rm dc}C\frac{{\rm d}U_{\rm dc}}{{\rm d}t} \tag{9}$$

$$\int (P - 1.5U_{\rm w}I_{\rm d}) \,\mathrm{d}t = CU_{\rm dc}^2 \tag{10}$$

As shown in Figure 5, the grid voltage decreases when a symmetrical fault occurs. When the voltage drops under 0.9 p.u., the OWT operates in the reactive current priority mode. And the OWT may lower its active current for more reactive current requirement from onshore grid. Thereby, the DC-link voltage increases according to (10), which may trigger the action of DC protection when the DC-link voltage exceeds the maximum value of U_{dcm} . The trajectory of the non-chopper method is O-A-B-G, as shown in Figure 6. With the utilization of the chopper, the chopper is put into operation when the DC-link voltage exceeds U_{chop} , and the maximum voltage is U_{dc1} . By adopting the chopper and the short-time overcurrent capacity of GSC, the DC-link voltage can be alleviated without raising the DC protection with maximum value of U_{dc2} , which is lower than U_{dc1} . The DC-link voltage trajectory is adjusted from line O-A-C-D-E to line O-F-G-H-E.



Figure 6. DC-link voltage trajectories under different control methods and voltage dip conditions.

In Figure 6, set voltage dip conditions: $U_{w0} \ge U_{w1} \ge U_{w2} \ge U_{w3}$. According to the Equation (9), the greater the voltage drops, the larger the unbalanced power, raising more apparent overcurrent features. So, an OWT under the grid voltage condition of U_{w1} easily reaches the chopper action voltage (U_{chop}), and the transient DC-link voltage is highest in the four conditions. The DC-link voltage comparison results are $U_{dc0} \le U_{dc1} \le U_{dc2} \le U_{dc3}$. We can also draw a conclusion that the DC-link voltage rise time is smaller than the DC voltage recovery time, which is apparent in Figure 6. By adopting GSC's short-term overcurrent capacity, the DC-link overvoltage can be significantly suppressed; meanwhile, the usage frequency and operation time of the chopper are reduced.

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3. Feasible Current Region of GSC

To better depict the feasible current region, we introduce four constraints, i.e., GSC capacity, pre-fault power ability, LVRT requirement, and synchronization stability.

(1) Constraint 1: GSC capacity.

 I_d and I_q are controlled in the LVRT controller by using the PI controllers. I_d and I_q are limited by the GSC capacity as follows:

$$I_d^2 + I_q^2 \le I_m^2 \tag{11}$$

(2) Constraint 2: pre-fault power ability.

The active power is determined by wind speed, and we assume that the pre-fault power ability is P_0 . Since the low-voltage transient process belongs to the millisecond level, the wind speed fluctuation can be ignored. When the onshore grid voltage drops from normal value (U_{w0}) to lower value, the active power of GSC may decrease. To keep the DC-link voltage stable according to (9), the active current is improved to a certain extent as follows, so the transient active current satisfies

$$I_{\rm d} = \frac{P_{\rm d0}}{a \cdot U_{\rm w}} = \frac{I_{\rm d0}}{a}$$
(12)

where *a* is the voltage drop degree, and i_{d0} is the pre-fault active current.

(3) Constraint 3: LVRT requirement.

When the onshore grid voltage drops in the interval of [0.2 p.u., 0.9 p.u.], the OWT employs the LVRT mode of reactive current priority. The reactive current reference is given according to (1):

$$I_{q} \ge -k_{q}(0.9 - U_{w}) \tag{13}$$

(4) Constraint 4: synchronization stability.

Maintaining synchronization with onshore grid is essential for OWTs during lowvoltage fault. Affected by long-distance transmission lines and high penetration of power electronic devices, the onshore grid tends to be weak in keeping voltage and frequency stable. Inexact current values of GSC can arise from the loss of synchronization in a weak grid or severe fault condition. According to the voltage stability analysis results of POI, the voltage fluctuation during LVRT period is affected by transient current of the OWT's GSC. Ensuring onshore grid voltage stability is the premise of allocating current distribution and supporting reactive power. Therefore, synchronization constraint should be fully considered in feasible current region depiction.

Based on Figure 2, we further establish the simplified circuit shown in Figure 7a when low-voltage fault occurs. According to the fault position in the onshore grid, we set Z_{s1} as the impedance vector from OWT to fault point. Z_{s1} is zero when nearby fault occurs. Z_{s2} is the impedance vector from onshore grid to fault point, and Z_{sf} is the fault impedance vector. Z_{s2} is zero when distant fault occurs. I_w is the injection current vector from OWT to fault point and I_s is the injection current vector from onshore grid to fault point. I_f is the grounding-fault current vector.

For the simplified circuit in Figure 7a, it can be equivalent to a simpler circuit with a voltage source and a resistor connected in series shown in Figure 7b by using the Thevenin's theorem. In the Figure 7b, the equivalent grid voltage vector U_{eq} and equivalent grid impedance vector Z_{eq} are affected by fault position, onshore grid voltage, and onshore grid impedance. R_{eq} and X_{eq} are the equivalent grid resistance and equivalent grid reactance of Z_{eq} , respectively. We can calculate U_{eq} and Z_{eq} according to

$$Z_{eq} = R_{eq} + jX_{eq} = Z_{s1} + \frac{Z_{s2}Z_{sf}}{Z_{s2} + Z_{sf}}$$

$$U_{eq} = \frac{Z_{sf}U_s}{Z_{s2} + Z_{sf}}$$
(14)



Figure 7. System circuit for calculating the fault voltage. (**a**) Simplified circuit during fault; (**b**) Thevenin's equivalent circuit.

The relationship between I_d , I_q , and AC voltage can be expressed as follows:

$$\boldsymbol{U}_{w} = \boldsymbol{I}_{w} \cdot \boldsymbol{Z}_{eq} + \boldsymbol{U}_{eq} = (\boldsymbol{I}_{d} + \boldsymbol{j}\boldsymbol{I}_{q}) \cdot (\boldsymbol{R}_{eq} + \boldsymbol{j}\boldsymbol{X}_{eq}) + \boldsymbol{U}_{eq}$$
(15)

The voltage feature is depicted in Figure 8. The *x*–*y* coordinate system is a synchronous rotational, and *x*-axis is oriented to U_{eq} . For simplification, I_w is controlled in the d–q coordinate system. δ is the phase of PLL's d-axis leading U_{eq} and ϕ is the power factor angle.



Figure 8. Vector diagram in rotational *x*–*y* and d–q coordinate system.

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Based on (5), there exists an inequality constraint of residual grid voltage, current references, and grid impedance, which is the precondition of the existence of PLL's equilibrium point [31] as represented by (16):

$$R_{\rm eq}I_{\rm q} + X_{\rm eq}I_{\rm d} = U_{\rm eq}\sin\delta \tag{16}$$

where U_{eq} is the equivalent grid impedance, namely the amplitude of U_{eq} . Since $-1 \le \sin \delta \le 1$, (17) can be further derived into [34]:

$$-U_{\rm eq} \le R_{\rm eq}I_{\rm q} + X_{\rm eq}I_{\rm d} \le U_{\rm eq} \tag{17}$$

Given equivalent grid voltage and grid impedance, the transient current regions are obtained based on (11) and (17). Then, considering that reactive current injection takes priority, and the remaining capacity is utilized for active current, the relationships can be depicted in three situations in Figure 9. When constraint 1 and constraint 4 are separated or tangent, the current region of GSC is only determined by constraint 1. While constraint 1 and constraint 4 are intersected, the current region of GSC is limited by the two constraints.

To better depict the current region of GSC, we set the condition that I_d is non-negative and I_q is non-positive during LVRT state:

$$\begin{aligned}
I_{\rm d} &\ge 0 \\
I_{\rm q} &\le 0
\end{aligned} \tag{18}$$



Figure 9. Relationships between constraint 1 and constraint 4: (a) Separation; (b) Tangent; (c) Intersection.

And we can further depict the current region in Figure 10, containing three situations: (a) $U_{eq} \ge X_{eq} \cdot I_m$; the absolute value of I_d and I_q both can reach I_m . (b) $R_{eq} \cdot I_m \le U_{eq} \le X_{eq} \cdot I_m$; only the absolute value of I_q both can reach I_m . (c) $U_{eq} \le R_{eq} \cdot I_m$; the absolute value of I_d and I_q all less than I_m . For situation (c), if we set I_q equals I_m , there is no stable equilibrium point in this region, which may cause a loss of synchronization problem.



Figure 10. Current region considering constraint 1 and constraint 4 under different equivalent grid impedance: (a) $U_{eq} \ge X_{eq} \cdot I_m$; (b) $R_{eq} \cdot I_m \le U_{eq} \le X_{eq} \cdot I_m$; (c) $U_{eq} \le R_{eq} \cdot I_m$.

As a result, we can finally obtain the feasible current region combining the four constraints. For steady state, the FCR is affected by GSC capacity, pre-fault power ability, and synchronization stability in Figure 11a, while for transient state, the FCR is affected by GSC capacity, pre-fault power ability, LVRT requirement, and synchronization stability in Figure 11b. Moreover, the current region can be enlarged by adopting the short-time overcurrent capacity of GSC. To avoid the OWT being off-grid or losing synchronization stability during low-voltage fault, it is necessary to ensure that the transient current references are in reasonable region. The proper current references can be given according to the designed FCR considering grid synchronization stability and grid code requirement.



Figure 11. Feasible current region during low-voltage fault: (a) steady state; (b) transient state.

4. Grid-Impedance-Based Transient Current Control

Based on the feasible current region, we set three situations for calculation the current references. The calculation method requires pre-fault parameters, including pre-fault power ability, equivalent grid impedance, equivalent grid voltage, and so on. The grid-impedance-based current calculation equations are determined as follows:

(1) Situation (a): $U_{eq} \ge X_{eq} \cdot I_m$

According to (1), the reactive current reference I_{gref} is

$$I_{\text{aref}} = -\min(k_{\text{q}} \cdot (0.9 - U_{\text{w}}), I_{\text{m}}), k_{\text{q}} \ge 1.5$$
(19)

Basically, the active current reference I_{dref} can be obtained:

$$I_{\rm dref} = \min\left(\frac{I_{\rm d0}}{U_{\rm w}}, \sqrt{I_{\rm m}^2 - I_{\rm qref}^2}\right) \tag{20}$$

(2) Situation (b): $R_{eq} \cdot I_m \leq U_{eq} \leq X_{eq} \cdot I_m$

The reactive current calculation refers to (19). According to (11) and (17), the transient active current reference can be derived as follows:

$$I_{\rm dref} = \min\left(\frac{I_{\rm d0}}{U_{\rm w}}, \sqrt{I_{\rm m}^2 - I_{\rm qref}^2}, \frac{U_{\rm eq} - R_{\rm eq}I_{\rm q}}{X_{\rm eq}}\right)$$
(21)

(3) Situation (c): $U_{eq} \leq R_{eq} \cdot I_m$

Substituting (1) and (20) into (6), the transient reactive current reference is

$$I_{\rm qref} = -\min\left(k_{\rm q} \cdot (0.9 - U_{\rm w}), I_{\rm m}, \frac{R_{\rm eq}U_{\rm eq} + X_{\rm eq}\sqrt{R_{\rm eq}^2 I_{\rm m}^2 + X_{\rm eq}^2 I_{\rm m}^2 - U_{\rm eq}^2}}{R_{\rm eq}^2 + X_{\rm eq}^2}\right), k_{\rm q} \ge 1.5$$
(22)

And the active current calculation refers to (21). To make the active current smoother and reduce the usage frequency of chopper, the short-term overcurrent capacity of GSC is adopted for current region expansion. We draw the I_d - U_w curve under situation (a) in Figure 12. We define the critical voltage as the maximum voltage at which the reactive current meets the LVRT requirement without sacrificing the active current. Substituting (1) and (12) into (11), the critical voltage U_2 can be given by the following equation:

$$\left(\frac{I_{d0}}{U_2}\right)^2 + \left(k_q \cdot (0.9 - U_2)\right)^2 = I_m^2$$
(23)



Figure 12. I_d - U_w curve under situation (a).

As depicted in Figure 12, the active current I_d varies with the OWT voltage. When the OWT voltage is within $[U_2, 1.0]$, I_d increases as the voltage decreases and reaches the maximum value of I_m when the voltage drops to U_2 . During the voltage of $[0.2, U_2]$, I_d decreases as the voltage drops. And I_d is zero when voltage is below 0.2 since the OWT is off-grid according to the grid code. Under situation (a), I_d can be obtained according to the red line in Figure 12. So, U_2 is the action voltage of chopper. If the POI voltage is less than U_2 , the chopper is put into operation for DC protection.

By establishing the equivalent circuit of faulty onshore grid integrated with the OWT and current calculation method based on feasible current region, we design the transient current control method depicted in Figure 13. Z_{eq} and U_{eq} are obtained by utilizing the grid impedance identification module and equivalent impedance calculation module. The obtained parameters are then sent to GSC controller to further calculate the desired active current reference and reactive current reference. Similar to the conventional LVRT method in Figure 4, the GSC control signal is generated through the inner-loop controller and pulse width modulation (PWM). Subsequently, by employing the proposed control method, the proper current references are provided, and DC-link overvoltage and synchronization instability are alleviated.



Figure 13. Schematic diagram of the grid-impedance-based transient current controller.

For the OWT's off-grid state verification, we set two judge conditions. One is the amplitude of voltage U_{w} . Another is the fault duration t_f . If the voltage drops under 0.2 p.u., the OWT is allowed to be off-grid. According to Figure 3, we can obtain the maximum tolerant time t_{fw} of different transient voltage:

$$t_{\rm fw} = \frac{55}{28} U_{\rm w} + \frac{13}{56} \tag{24}$$

The flowchart of the step-by-step process of proposed transient current control method is depicted in Figure 14. It can be seen that there are several steps:

- (1) Identifying onshore grid fault and collecting fault parameters, i.e., faulty point, faulty degree, onshore gird impedance, voltage, active/reactive current of the OWT.
- (2) Judging the operation status of the OWT according to U_w . If $0.9 \le U_w \le 1.0$, the OWT adopts steady control mode of active current priority. If $0.2 \le U_w \le 0.9$, the OWT adopts LVRT control mode of reactive current priority. Otherwise, the OWT is off-grid due to the deep voltage drop.
- (3) Calculating the equivalent grid impedance and equivalent grid voltage U_{eq} .
- (4) Calculating active current and reactive current of GSC according to the three situations in Figure 10.
- (5) Sending control signals to GSC. As indicated, by employing the proposed method during low-voltage fault, the proper current reference is obtained to support onshore grid voltage and ensure synchronization stability.



Figure 14. Flowchart of proposed method.

5. Simulation Results

5.1. System Parameters

To verify the correctness of the proposed control method, the simulation model is carried out with a 5 MW PMSG-based OWT in PSCAD environment. The schematic structure and detailed parameters are presented in Figure 2 and Table 1. A short-circuit ratio (SCR) is adopted to describe the grid strength [35], and onshore grid with a low SCR presents higher grid impedance, deeper voltage drops and slower voltage recovery compared to onshore grid with larger SCR.

Table 1. OWT parameters.

Parameters	Value
Rated power/MW	5.0
Rated AC voltage/kV	0.69
Rated wind speed/m/s	11.4
DC-link voltage/kV	1.5
System frequency/Hz	50

In the case study, comparison and discussion are given between the transient current control of adopting the traditional LVRT method (Method 1) in [36] and the proposed method (Method 2). Five cases are chosen for validating the performance of the proposed method, and listed in Table 2:

 $SCR = 10, X_s / R_s = 3$

 $SCR = 5.0, X_s / R_s = 3$

 $SCR = 1.5, X_s / R_s = 3$

 $SCR = 1.5, X_s/R_s = 3$

Case No. Case 1

Case 2

Case 3

Case 4

Case 5

Table 2. Cases settings.				
Grid Strength	Pre-Fault Active Power	Fault Impedance	Fault Voltage	Fault Duration
$SCR = 10, X_s/R_s = 3$	1.0 p.u.	0.0390 p.u.	0.8 p.u.	0.2 s

0.0053 p.u.

0.0142 p.u.

0.0063 p.u.

0.0069 p.u.

0.4 p.u.

0.7 p.u.

0.8 p.u.

0.6 p.u.

5.2. *Case* 1: *Strong Grid* (SCR = 10), $P_0 = 1 p.u.$, $U_w = 0.8 p.u.$

1.0 p.u.

0.5 p.u.

1.0 p.u.

1.0 p.u.

The simulated results are shown in Figure 15. In this simulation, a three-phase fault occurs at 1.5 s, bringing slight decline of the POI voltage from 1 p.u. to 0.8 p.u in Figure 15a. From Figure 15b, the DC-link voltage of the OWT increases to 1.58 kV, which is far lower than its protection value of 1.95 kV (1.3 p.u.). According to the grid-impedance-based current calculation equations of proposed method, the OWT operates in situation (a) as it satisfies $U_{eq} \ge X_{eq} \cdot I_m$. And the transient currents in Figure 15c are calculated as $I_q = -0.15$ p.u., $I_d = 1.19$ p.u. During the transient state, the active current is determined by GSC capacity. Affected by the voltage sag, active power drops from 1 p.u. to 0.952 p.u., and reactive power rises from 0 p.u. to 0.12 p.u. After the fault is cleared, the OWT can recover to the steady state.





5.3. *Case 2: Strong Grid (SCR = 10),* $P_0 = 1 p.u., U_w = 0.4 p.u.$

Figure 16 shows the simulation wave forms of the PMSG-based OWT during the POI voltage at 0.4 p.u. For the condition of deep onshore voltage dips, the DC-link voltage is 1.76 kV, increasing the voltage to 1.17 times in Figure 16b, and the chopper is adopted to alleviate the overvoltage. The OWT operates in situation (a), which is consistent with the stability analysis process in Figure 15. From Figure 16c, active current and reactive current are 0.937 p.u. and -0.75 p.u., respectively. Furthermore, we can obtain the active power of 0.375 p.u. and reactive current of 0.3 p.u. in Figure 16d. Combined with Figure 15, reactive current and DC-link voltage of the OWT both rise with the decline of POI voltage. The more voltage drops, the more severe the DC-link voltage is. The simulation results verify the correctness of the analysis in Sections 2 and 3.

0.2 s

0.2 s

 $1.0 \mathrm{s}$

 $1.0 \mathrm{s}$



Figure 16. Simulation results of case 2. (**a**) POI voltage of OWT, (**b**) DC-link Voltage, (**c**) active and reactive current of OWT, and (**d**) active and reactive power of OWT.

5.4. *Case* 3: *Strong Grid* (SCR = 5), $P_0 = 0.5 \text{ p.u.}, U_w = 0.7 \text{ p.u.}$

Similarly, the influence of pre-fault power ability on system stability is investigated. Figure 17 depicts the simulation results of the OWT during POI voltage at 0.7 p.u. According to Figure 17b, the DC-link voltage varies slightly as GSC can produce more active current to compensate for the power drop issue caused by voltage drop. Active current is initially set as 0.5 p.u. and rises to 0.714 p.u. during fault. Reactive current varies from 0 to -0.3 p.u., as shown in Figure 17c according to the grid-impedance-based current calculation equations. Thus, the active power remains at a steady value of 0.5 p.u. and reactive power improves to 0.21 p.u. For the non-rated power situation, the OWT can provide reactive current support for onshore gird while producing more active current to suppress the DC-link overvoltage issue during the low-voltage fault.



Figure 17. Simulation results of case 3. (**a**) POI voltage of OWT, (**b**) DC-link Voltage, (**c**) active and reactive current of OWT, and (**d**) active and reactive power of OWT.

5.5. Case 4: Weak Grid (SCR = 1.5), $P_0 = 1 p.u.$, $U_w = 0.8 p.u.$

Case 4 is to verify the method under weak grid with slight voltage sag. According to Figure 18a, the onshore gird voltage drops to 0.756 p.u. and the POI voltage drops to 0.8 p.u., both in Method 1 and Method 2. In Figure 18c, the DC-link voltages of two methods are 1.62 p.u. and 1.55 p.u., respectively. So, the OWT with Method 1 needs to withstand more severe overvoltage. With Method 1, the active current and reactive current are 0.989 p.u. and -0.15 p.u., respectively. In Method 2, the OWT operates in situation (a). The active current is 1.19 p.u., which is larger than the value of Method 1 in Figure 18d. Furthermore, the active power of Method 1 is 0.79 p.u., which is lower than the active power of 0.95 p.u. in Method 2. The value of reactive current is same with that of Method 1, so the reactive power is 0.12 p.u. both in Method 1 and Method 2. Compared to Method 1, OWTs adopting Method 2 can produce more active power for the onshore grid in the transient period.



Figure 18. Simulation results of case 4. (**a**) Voltage of onshore grid, (**b**) POI voltage of OWT, (**c**) DC-link Voltage, (**d**) active and reactive current of OWT, (**e**) active power of OWT, and (**f**) reactive power of OWT.

5.6. *Case 5: Weak Grid (SCR = 1.5), P* $_0$ = 1 *p.u., U* $_w$ = 0.6 *p.u.*

The simulation is carried out in the condition of SCR = 1.5 and deep voltage dip of onshore grid shown in Figure 19. The system response with Method 1 and Method 2 are compared in Figure 19b–f, respectively. According to (24), when the POI voltage drops to 0.6 p.u., the maximum tolerant time is 1.41 s, which is longer than the simulation fault time of 1 s. Obvious oscillations get to occur in the DC-link voltage, transient current, and transient power, implying that the system loses its stability with Method 1. So, Method 1 is not suitable for OWTs in weak grids with deep voltage drop. From Figure 19b, the POI voltage drops to 0.6 p.u., and it is calculated that $R_{eq} \cdot I_m \leq U_{eq} \leq X_{eq} \cdot I_m$ according

to the grid-impedance-based current calculation equations. So, the OWT is in the region of situation (b) with Method 2. The active current and reactive current are 0.783 p.u. and -0.45 p.u., respectively, while in Method 1, the active value of 0.89 exceeds the feasible transient region, which causes system instability. The maximum value of active power and reactive power are 0.47 p.u. and 0.27 p.u. during the steady state of LVRT, respectively. By calculating the transient grid impedance, the proper current references are given to GSC controller, avoiding OWT instability during weak grid with low-voltage fault. Thus, compared to the conventional method, the DC-link overvoltage and power oscillations are suppressed significantly by adopting the proposed transient current control method.



Figure 19. Simulation results of case 5. (a) Voltage of onshore grid, (b) POI voltage of OWT, (c) DC-link Voltage, (d) active and reactive current of OWT, (e) active power of OWT, and (f) reactive power of OWT.

6. Conclusions

In this paper, we propose a novel transient current control method for offshore wind turbines that takes into account the grid-impedance-based feasible current region to enhance the system's transient stability. Several important findings can be summarized as follows:

(1) The proposed solution combining GSC's short-time overcurrent capacity and chopper can mitigate the DC-link overvoltage arising from unbalanced power during low-voltage fault.

(3) The proposed method suppresses power oscillations and ensures voltage stability more effectively compared to the traditional LVRT method. Extensive simulations have confirmed its excellent performance in transient stability under different voltage dip or grid impedance conditions, without requiring additional hardware investment.

Moreover, it should be noted that the transient stability of a single OWT is not solely determined by the feasible current region, but also by the interactive behavior of other OWTs in the farm. Future research will focus on fault detection and decentralized autonomous control of OWTs.

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